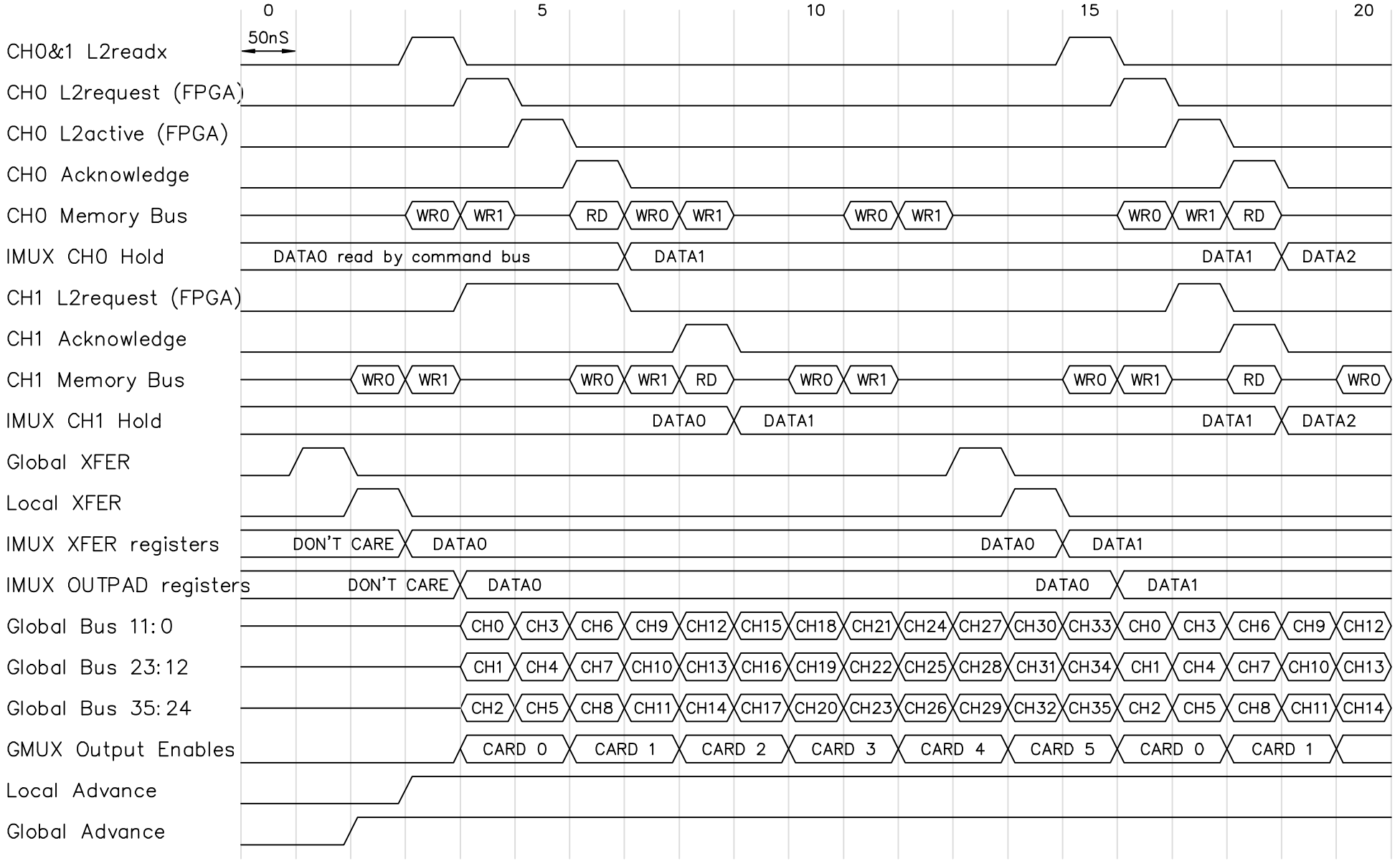
EMC Electronics 5/9/00 Draft

1.1.3.2.1 Tower Data Collector Memory Read & GLink Bus Timing for L2