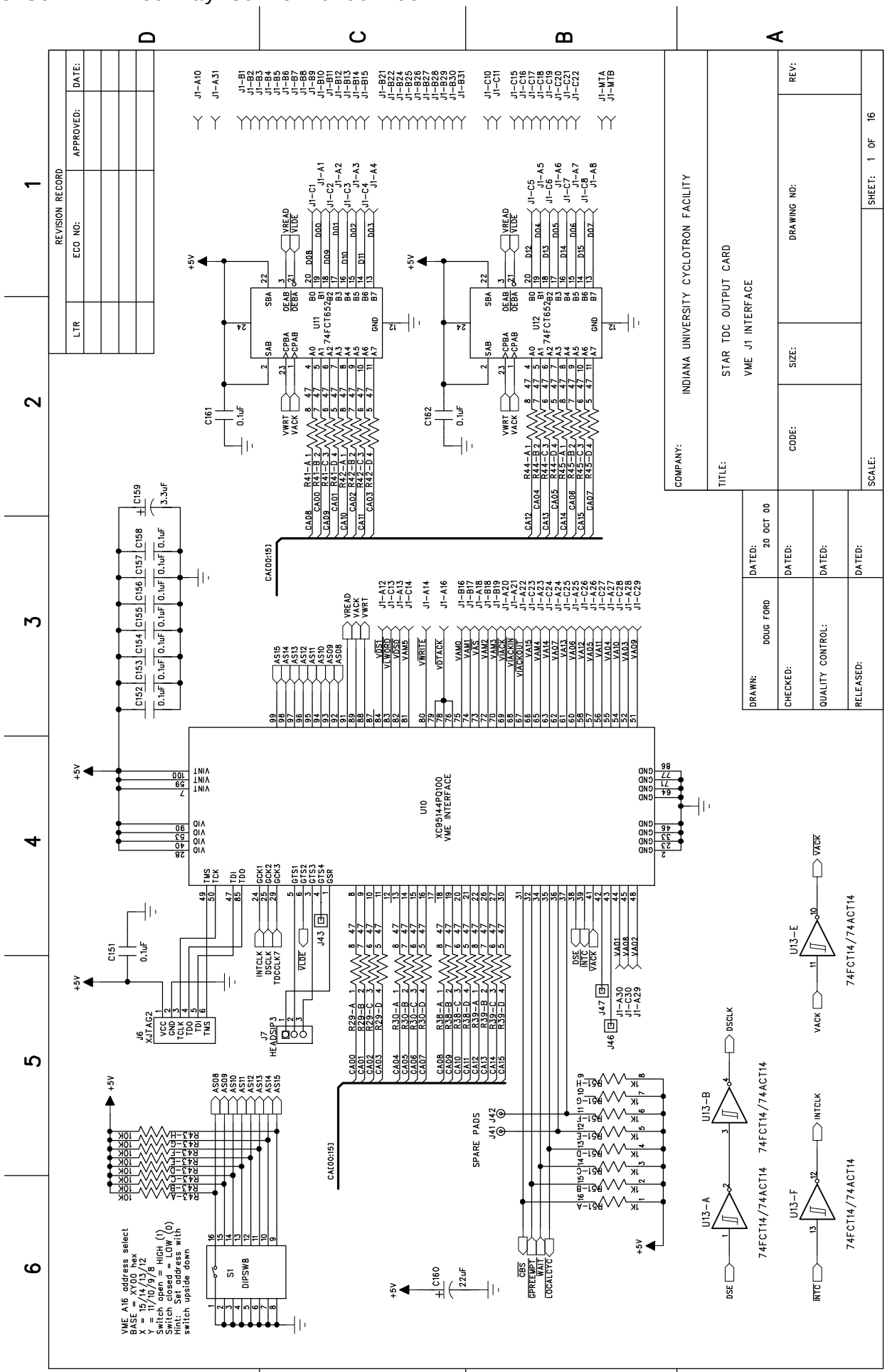
REVISION RECORD
LTR
ECO NO:
APPROVED:
DATE:
U10
XC95144PQ100
VME INTERFACE
U11
74FCT652
U12
74FCT652
U13-A
U13-B
U13-E
U13-F
74FCT14/74ACT14
J6
XJTAG2
J7
HEADSIP3
J43
J46
J47
J41 J42
SPARE PADS
S1
DIPSW8
VME A16 address select
BASE = XY00 hex
X = 15/14/13/12
Y = 11/10/9/8
Switch open = HIGH (1)
Switch closed = LOW (0)
Hint: Set address with switch upside down
CA00:15
COMPANY:
INDIANA UNIVERSITY CYCLOTRON FACILITY
TITLE:
STAR TDC OUTPUT CARD
VME J1 INTERFACE
DRAWN:
DOUG FORD
DATED:
20 OCT 00
CHECKED:
QUALITY CONTROL:
RELEASED:
CODE:
SIZE:
DRAWING NO:
REV:
SCALE:
SHEET: 1 OF 16