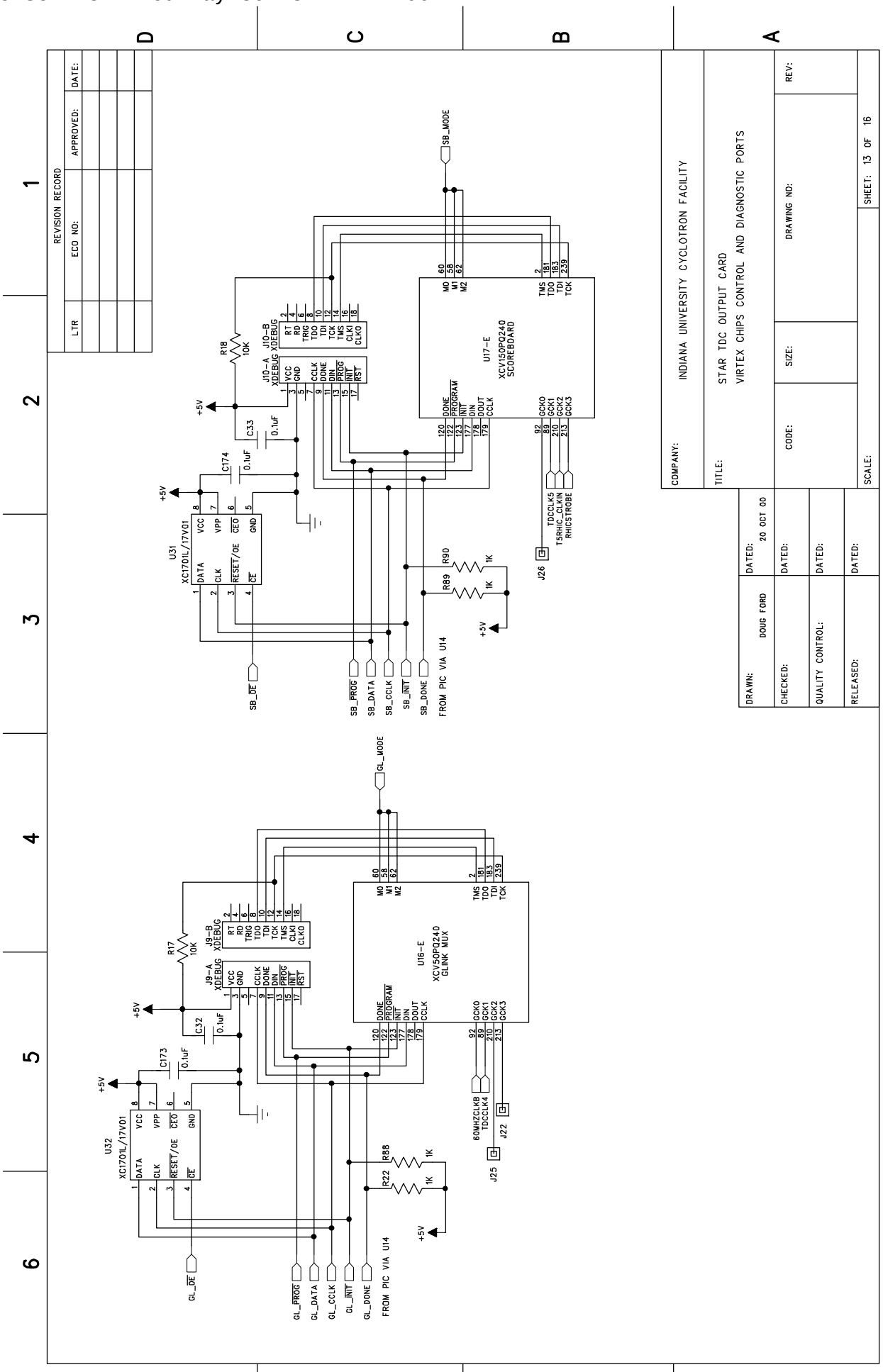
REVISION RECORD
LTR
ECO NO:
APPROVED:
DATE:
COMPANY: INDIANA UNIVERSITY CYCLOTRON FACILITY
TITLE: STAR TDC OUTPUT CARD
VIRTEX CHIPS CONTROL AND DIAGNOSTIC PORTS
CODE:
SIZE:
DRAWING NO:
REV:
SCALE:
SHEET: 13 OF 16
DRAWN: DOUG FORD
DATED: 20 OCT 00
CHECKED:
QUALITY CONTROL:
RELEASED:
SB_MODE
GL_MODE
U17-E
XCV150PQ240
SCOREBOARD
U16-E
XCV50PQ240
GLINK MUX
U31
XC1701L/17V01
U32
R18 10K
R17 10K
R90 1K
R89 1K
R88 1K
R22 1K
C33 0.1uF
C174 0.1uF
C32 0.1uF
C173 0.1uF
J10-A XDEBUG
J10-B XDEBUG
J9-A XDEBUG
J9-B XDEBUG
J26
J25
J22
SB_OE
SB_PROG
SB_DATA
SB_CCLK
SB_INIT
SB_DONE
FROM PIC VIA U14
GL_OE
GL_PROG
GL_DATA
GL_CCLK
GL_INIT
GL_DONE
FROM PIC VIA U14
TDCCLK5
TSRHIC_CLKIN
RHICSTROBE
60WHZCLKB
TDCCLK4
+5V